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PATENT

**IN THE UNITED STATES PATENT & TRADEMARK OFFICE**

Applicants: Seung-hoon LEE

Examiner: Miner Pho LUU

Serial No.: 10/731,841

Art Unit: 2824

Filed: December 9, 2003

Docket: 8021-182 (SS-18122-US)

Title: SENSE AMPLIFYING CIRCUIT FOR A SEMICONDUCTOR MEMORY  
WITH IMPROVED DATA READ ABILITY AT A LOW SUPPLY VOLTAGE

Mail Stop Amendment  
Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

**AMENDMENT**

This paper is in response to the Non-Complaint Amendment dated February 10, 2006. The Examiner is requested to enter and consider the following amendments and remarks.

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**CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8(a)**

I hereby certify that this correspondence (and any document referred to as being attached or enclosed) is being deposited with the United States Postal Service as first class mail, postage paid in an envelope addressed to: Commissioner for Patents, Alexandria, VA 22313-1450 on the date indicated below:

Date: February 27, 2006

By: \_\_\_\_\_

Frank Chau